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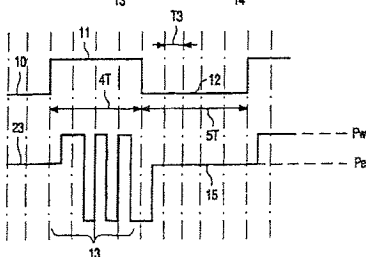
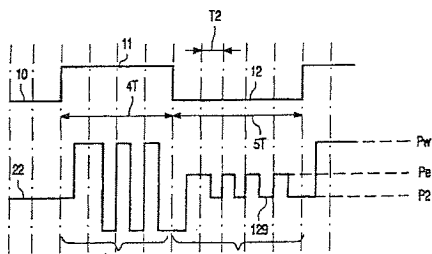
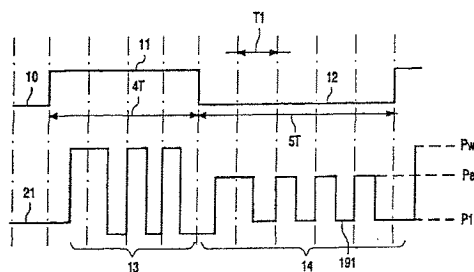
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(54) Title: METHOD AND DEVICE FOR RECORDING AN INFORMATION SIGNAL ON AN INFORMATION LAYER OF A RECORDING MEDIUM



(57) Abstract: The invention relates to a method and a device for recording an information signal (10) in a phase change layer of a recording medium. A mark is recorded by a sequence (13) of write pulses and previous marks between the marks to be recorded are erased by a sequence of erase pulses (14). The bias power level (191) between the erase pulses is made dependent on the recording speed, thus obtaining low jitter of the recorded marks and at the same time preventing thermal cross write during erase.



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*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

Method and device for recording an information signal on an information layer of a recording medium.

The invention relates to a method of recording at a recording speed an information signal on an information layer of a recording medium by irradiating the information layer by means of a radiation beam, said information layer having a phase reversibly changeable between a first state and a second state, which method comprises a  
5 write step for applying the radiation beam, in response to the information signal, to a first area of the information layer to cause the first area of the information layer to assume the first state, thereby forming a mark, and an erase step for applying a pulsed radiation beam to a second area of the information layer, before and after the mark, to cause the second area of the information layer to assume a state substantially identical to the second state, the pulsed  
10 radiation beam comprising erase pulses having an erase power level ( $P_e$ ) and a bias power level ( $P_b$ ) between the erase pulses.

The invention also relates to a recording device for recording at a recording speed an information signal on an information layer of a recording medium by irradiating the information layer by means of a radiation beam, said information layer having a phase  
15 reversibly changeable between a first state and a second state, the device comprising a radiation source for providing the radiation beam and control means for controlling the power of the radiation beam such that in response to the information signal the radiation beam is applied to a first area of the information layer to cause the first area of the information layer to assume the first state, thereby forming a mark, and such that a pulsed radiation beam  
20 comprising erase pulses having an erase power level ( $P_e$ ) and a bias power level ( $P_b$ ) between the erase pulses is applied to a second area of the information layer, before and after the mark, to cause the second area of the information layer to assume a state substantially identical to the second state.

25

A recording method of the kind set forth in the preamble is known from United States patent 5,291,470. A mark is written by a sequence of write pulses and the previously written marks between the marks being written are erased by applying a sequence of erase pulses. The write pluses are at a write power level ( $P_w$ ) and the erase pulse are at an

erase power level ( $P_e$ ), the erase power level ( $P_e$ ) being lower than the write power level ( $P_w$ ). The power level between the write pulses as well as the erase pulses is a bias power level ( $P_b$ ). In general, this bias power level ( $P_b$ ) is set equal to the reading power level ( $P_o$ ).

The above method is known as a "pulsed erase" recording method as opposed  
5 to a recording method where the previously written marks between the marks being written are erased by applying a radiation beam having a constant erase power level. Because of this constant erase power level, the temperature of the information layer rises gradually. This temperature rise leads to erasure of not only marks in the actual track, but also of marks in adjacent tracks. This so-called "thermal cross-write" effect is reduced by applying the pulsed  
10 erase method.

It is a drawback of the "pulsed erase" recording method that it does not achieve sufficiently low jitter in the read signal obtained from reading marks recorded by using the "pulsed erase" method, especially when the marks are recorded at a variable recording speed. The jitter is the standard deviation of the time differences between level  
15 transitions in a digitized read signal and the corresponding transitions in a clock signal, the time difference being normalized by the duration of one period of said clock.

The recording speed may vary, for example, when recording at a Constant Angular Velocity (CAV). In this case the recording speed, i.e. the speed at which the recording medium moves relative to the spot of the radiation beam on the recording medium,  
20 increases when going from the center towards the periphery of a disc shaped recording medium. The recording speed is also known as the Linear Velocity.

It is an object of the invention to provide a method according to the opening  
25 paragraph for recording marks with reduced jitter while preventing "thermal cross-write" during erase.

This object is achieved by a method of the kind set forth which is characterized in that the bias power level ( $P_b$ ) depends on the recording speed.

When the bias power level ( $P_b$ ) is set at a fixed level no thermal cross write  
30 occurs while a minimal jitter of the recorded marks is obtained at a specific recording speed only. However, when the bias power level ( $P_b$ ) is set in dependence on the recording speed, minimal jitter of the recorded marks may be obtained for each recording speed while still preventing the occurrence of thermal cross write to occur.

A more optimum setting can be obtained by introducing additional degree of freedom in the settings for the sequence of erase pulses (i.e., a variable bias power level instead of a fixed one), resulting in a reduced jitter of the recorded marks.

A version of the method according to the invention is characterized in that the  
5 bias power level ( $P_b$ ) increases in a range between zero and the erase power level ( $P_e$ ) as the recording speed ( $V$ ) increases.

During recording at a low recording speed the spot of the radiation beam on the recording medium will remain comparatively long in one specific area of the recording medium compared to recording taking place at a high recording speed. Therefore, during  
10 recording at a low recording speed the area on the recording medium will experience a significant temperature rise resulting in a significant thermal cross-write. This temperature rise, and hence the thermal cross write, is reduced when a low bias power level ( $P_b$ ) is used. However, during recording at a high recording speed the spot of the radiation beam on the recording medium will remain in one specific area of the recording medium for a  
15 comparatively short time only. Therefore, a higher bias power level ( $P_b$ ), even up to the erase power level ( $P_e$ ), may be used without introducing thermal cross write. Application of a high bias power level ( $P_b$ ) results in low jitter of the recorded marks.

A preferred version of the method according to the invention is characterized in that the bias power level ( $P_b$ ) increases in a range between zero and the erase power level  
20 ( $P_e$ ) as the recording speed ( $V$ ) increases when the recording speed is below a chosen recording speed, and that the bias power level ( $P_b$ ) is substantially identical to the erase power level ( $P_e$ ) when the recording speed exceeds the chosen recording speed.

Beyond a certain recording speed no thermal cross write occurs, because the spot of the radiation beam on the recording medium remains only very briefly in one specific  
25 area of the recording medium. Beyond this recording speed the bias power level ( $P_e$ ) is set substantially equal to the erase power level ( $P_e$ ), resulting in a radiation beam having a constant erase power level. Because of this constant erase power level a. Jitter of the recorded marks is minimized.

A version of the method according to the invention is characterized in that the  
30 duty cycle of the erase pulses depends on the recording speed.

In addition to changing the bias power level ( $P_b$ ), the amount of energy transferred from the radiation beam to the recording medium, and hence the local temperature rise of the recording medium, can also be influenced by changing the duty cycle of the erase

pulses. Both jitter of the recorded marks and thermal cross can both be minimized by setting the duty cycle of the erase pulses in dependency of the recording speed.

A version of the method according to the invention is characterized in that the  
5 duty cycle increases in a range between nearly zero and unity as the recording speed (V) increases.

During recording at a low recording speed, the spot of the radiation beam on the recording medium will remain comparatively long in one specific area of the recording medium compared to when recording takes place at a high recording speed. Therefore, during  
10 recording at a low recording speed the area on the recording medium will experience a significant temperature rise resulting in a significant thermal cross-write. This temperature rise, and hence the thermal cross write, is reduced when a smaller cycle of the erase pulses is used. However, during recording at a high recording speed, the spot of the radiation beam on the recording medium will remain in one specific area of the recording medium for a  
15 comparatively short time. Therefore, a higher duty cycle of the erase pulses, even up to the unity, may be used without giving rise to thermal cross write.

It is to be noted that the above methods according to the invention offer especially good results when used in combination with a recording medium having an information layer where the recrystallization process of the materials is determined by growth  
20 from the edges of amorphous marks. These so-called Fast Growth Materials (FGM) allow for high recording speeds but are sensitive to thermal cross write.

It is a further object of the invention to provide a recording device of the kind described in the opening paragraph which is arranged to carry out the method according to the invention.

25 This object is achieved by a recording device of the kind set forth in the preamble which is characterized in that the control means set the bias power level ( $P_b$ ) in dependence on the recording speed (V).

A preferred embodiment of the recording device according to the invention is characterized in that the control means set the bias power level ( $P_b$ ) so that it increases in a  
30 range between zero and the erase power level ( $P_e$ ) as the recording speed (V) increases when the recording speed is below a chosen recording speed and that the control means set the bias power level ( $P_b$ ) so as to be substantially identical to the erase power level ( $P_e$ ) when the recording speed is above the chosen recording speed.

The objects, features and advantages of the invention will be apparent from the following, more specific description of embodiments of the invention as illustrated in the accompanying drawings; therein:

5 Figure 1 shows diagrams illustrating the time-dependency of the information signal and of the control signal controlling the power levels of the radiation beam,

Figure 2 shows a graph representing a setting of the bias power level as a function of the recording speed, and

Figure 3 shows a diagram of a recording device according to the invention.

10 Figure 1 shows diagrams with two types of signals, that is, an information signal 10 and control signals 21, 22 and 23 controlling the power of the radiation beam. The diagrams are shown at an identical scale. The vertical dashed lines in these diagrams indicate  
15 transitions in a clock signal of a data clock belonging to the information signal. The period of the data clock, also called a channel bit period, is indicated by  $T_1$ ,  $T_2$ , and  $T_3$ , respectively.  $T_1$  is the length of one period of the data clock when recording the information signal 10 at a first recording speed.  $T_2$  is the length of one period of the data clock when recording the information signal 10 at a higher recording speed amounting to approximately 1.4 times the  
20 first recording speed ( $T_2 = T_1 / 1.4$ ) and  $T_3$  is the length of one period of the data clock when recording the information signal 10 at an even higher recording speed of approximately 1.75 times the first recording speed ( $T_3 = T_1 / 1.75$ ).

When recording the information signal 10, a 'high' period 11 is recorded as a mark and a 'low' period 12 is recorded as an unwritten area (a space) between the marks. A  
25 mark is recorded on an information layer of a recording medium by irradiating the information layer by means of a sequence of write pulses 13 having a write power level ( $P_w$ ). A space is formed by irradiating the information layer by means of a sequence of erase pulses 14 having a erase power level ( $P_e$ ) or by irradiating the information layer by means of a radiation beam having a constant erase power level 15 ( $P_e$ ) thus erasing any previously  
30 recorded marks. This mechanism of erasing a previously recorded information signal while at the same time recording a new information signal is known as Direct Overwrite.

The shapes of both the write pulse sequence 13 and the erase pulse sequence 14 are shown as examples. Other shapes, for example comprising an alternative number of pulses, may also be used.

Figure 1a shows the control signals 21 controlling the power of the radiation beam when recording the information signal 10 at a comparatively slow first recording speed. The bias power level ( $P_b$ ) 191 is set at a power level  $P_1$  such that no thermal cross write effects occur when recording at this recording speed.

5 Figure 1b shows the control signals 22 controlling the power of the radiation beam when recording the information signal 10 at a higher recording speed amounting to approximately 1.4 times the first recording speed. The bias power level ( $P_b$ ) 192 is set at a power level  $P_2$ . At this power level  $P_2$  no thermal cross write effects occur when recording at this higher speed. Moreover, jitter of the recorded marks is reduced because  $P_2$  is higher than  
10  $P_1$ .

Figure 1c shows the control signals 23 controlling the power of the radiation beam when recording the information signal 10 at an even higher recording speed amounting to approximately 1.75 times the first recording speed. At this recording speed the bias power level ( $P_b$ ) is set equal to the erase power level  $P_e$ . Because of the high recording speed the  
15 spot of the radiation beam on the recording medium will remain only comparatively briefly in one specific area of the recording medium. Because of this, no thermal cross write effect will occur, not even when a constant erase power level  $P_e$  is applied. Applying a constant erase power level  $P_e$  will result in a minimized jitter of the recorded marks.

Figure 2 shows an example of a setting of the bias power level  $P_b$  as a function  
20 of the recording speed  $V$ . The horizontal axis 26 of the graph represent the value of the bias power level  $P_b$  scaled to the erase power level  $P_e$  (i.e.,  $P_B = P_b / P_e$ ) and the horizontal axis 27 of the graph represents a normalized recording speed  $V'$ . Below a chosen recording speed 29 the bias power level  $P_b$  increases as the recording speed increases. Above the chosen recording speed 29, the bias power level  $P_b$  is set equal to the erase power level  $P_e$  for all  
25 remaining recording speeds.

Figure 3 shows a recording device according to the invention for recording an information signal 10 on a disc-shaped recording medium 30 comprising an information layer 301. The recording medium 30 is rotated around its center by a motor 34. A radiation beam 32 is generated by a radiation source 31 and focused onto the information layer 301 by a lens  
30 33.

The information signal 10 is applied to control means 60. A current source 61 within the control means 60 has four outputs, A, B, C and D. Output A provides a current which, when fed to the radiation source 31 through a control signal  $S_C$ , will result in the radiation beam 32 having an erase power level  $P_e$ . Likewise, outputs B, C and D provide



currents resulting in the bias power level  $P_b$ , the write power level  $P_w$ , and the power level in between the write pulses, respectively. The current of each output A, B, C and D can be selected by a switch-unit 62. The switch-unit 62 is operated by a pattern generator 63 controlled by the information signal 10. The pattern generator 63 transforms the information  
5 signal into a control signal  $S_C$  comprising sequences of write pulses and sequences of erase pulses in conformity with the desired pattern

The control means 60 also comprise setting means 65 for setting the current of output B of the current source 61 in dependence on the recording speed. Setting the current of output B enables the bias power level  $P_b$  to be set in dependence on the recording speed.  
10 During rotation of the recording medium 30 at a Constant Angular Velocity (CAV), the recording speed  $V$  depends on the radial position of the focused radiation beam 32 on the recording medium 30. At the periphery of the recording medium, the recording speed is at its highest and it is at its lowest at the center of the recording medium. The radial position of the radiation source 31 is controlled by positioning means 35. Information about the radial  
15 position of the radiation source 31 is transferred from position means 35 to setting means 65. The setting means 65 determine the actual settings for the current of output B of the current source 61 on the basis of this information.

Figure 3 shows an embodiment of a recording device according to the invention. However, it will be apparent to those skilled in the art that embodiments of a  
20 recording device according to the invention using more power levels can be realized by adding additional outputs to the current source 61 and by extending the switch-unit 62. Moreover, it will be apparent that a single output B of the current source 61 providing a varying current can, as an alternative, be replaced by several outputs each providing a different current while the setting means 65 select the appropriate output for setting the bias  
25 power level  $P_b$  at any time.

## CLAIMS:

1. A method of recording at a recording speed ( $V$ ) an information signal on an information layer of a recording medium by irradiating the information layer by means of a radiation beam, said information layer having a phase reversibly changeable between a first state and a second state, which method comprises
  - 5 - a write step for applying the radiation beam, in response to the information signal, to a first area of the information layer to cause the first area of the information layer to assume the first state, thereby forming a mark, and
  - an erase step for applying a pulsed radiation beam to a second area of the information layer, before and after the mark, to cause the second area of the information layer
  - 10 to assume a state substantially identical to the second state, the pulsed radiation beam comprising erase pulses having a erase power level ( $P_e$ ) and a bias power level ( $P_b$ ) between the erase pulses,
  - characterized in that the bias power level ( $P_b$ ) depends on the recording speed.
- 15 2. A method as claimed in claim 1, characterized in that the first state is an amorphous state and the second state is a crystalline state.
3. A method as claimed in claim 1, characterized in that the bias power level ( $P_b$ ) increases in a range between zero and the erase power level ( $P_e$ ) as the recording speed ( $V$ )
- 20 increases.
4. A method as claimed in claim 1, characterized in that the bias power level ( $P_b$ ) increases in a range between zero and the erase power level ( $P_e$ ) as the recording speed ( $V$ ) increases when the recording speed is below a chosen recording speed, and that the bias
- 25 power level ( $P_b$ ) is substantially identical to the erase power level ( $P_e$ ) when the recording speed exceeds the chosen recording speed.

5. A method as claimed in claim 1 wherein the erase pulses have a duty cycle of  $T_e/T_b$ , where  $T_e$  is the duration of an erase pulse and  $T_b$  is the time between two successive erase pulses, characterized in that the duty cycle depends on the recording speed.

5 6. A method as claimed in claim 5, characterized in that the duty cycle increases in a range between nearly zero and unity as the recording speed (V) increases.

7. A recording device for recording at a recording speed (V) an information signal on an information layer of a recording medium by irradiating the information layer by means of a radiation beam, said information layer having a phase reversibly changeable between a first state and a second state, the device comprising a radiation source for providing the radiation beam and control means for controlling the power of the radiation beam such that

10 - in response to the information signal, the radiation beam is applied to a first area of the information layer to cause the first area of the information layer to assume the first state, thereby forming a mark, and such that

15 - a pulsed radiation beam comprising erase pulses having an erase power level ( $P_e$ ) and a bias power level ( $P_b$ ) between the erase pulses is applied to a second area of the information layer, before and after the mark, to cause the second area of the information layer

20 to assume a state substantially identical to the second state, characterized in that the control means set the bias power level ( $P_b$ ) in dependence on the recording speed (V).

8. A recording device as claimed in claim 7, characterized in that the control means set the bias power level ( $P_b$ ) so that it increases in a range between zero and the erase power level ( $P_e$ ) as the recording speed (V) increases.

25

9. A recording device as claimed in claim 7, characterized in that the control means set the bias power level ( $P_b$ ) so that it increases in a range between zero and the erase power level ( $P_e$ ) as the recording speed (V) increases when the recording speed is below a chosen recording speed and that the control means set the bias power level ( $P_b$ ) so as to be substantially identical to the erase power level ( $P_e$ ) when the recording speed is above the chosen recording speed.

30

10. A recording device as claimed in claim 7, wherein the erase pulses have a duty cycle of  $T_e/T_b$ , where  $T_e$  is the duration of an erase pulse and  $T_b$  is the time between two successive erase pulses, characterized in that the control means set the duty cycle in dependence on the recording speed.

5

11. A recording device as claimed in claim 10, characterized in that control means set the duty cycle so as to increase in a range between nearly zero and unity as the recording speed ( $V$ ) increases.

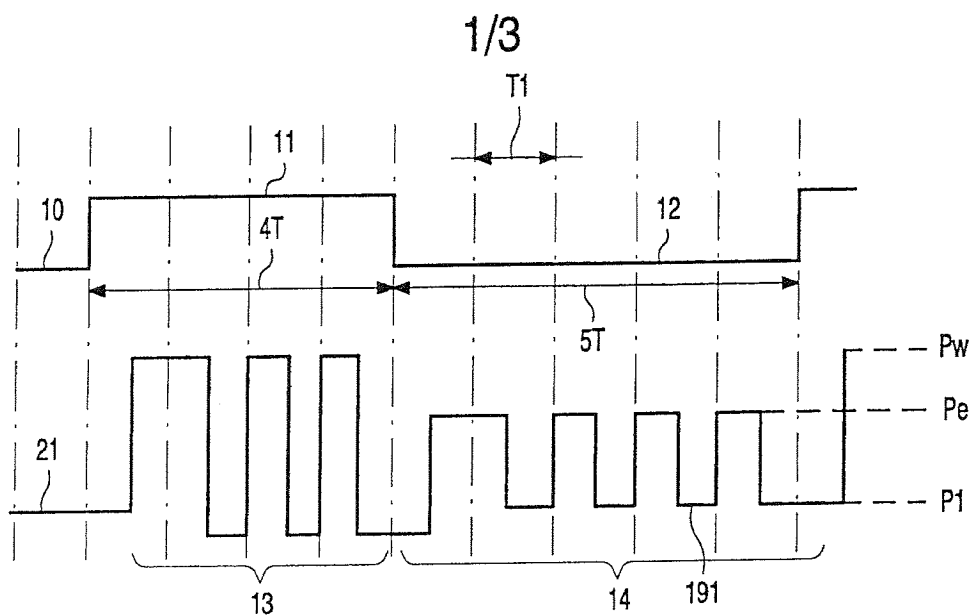


FIG. 1A

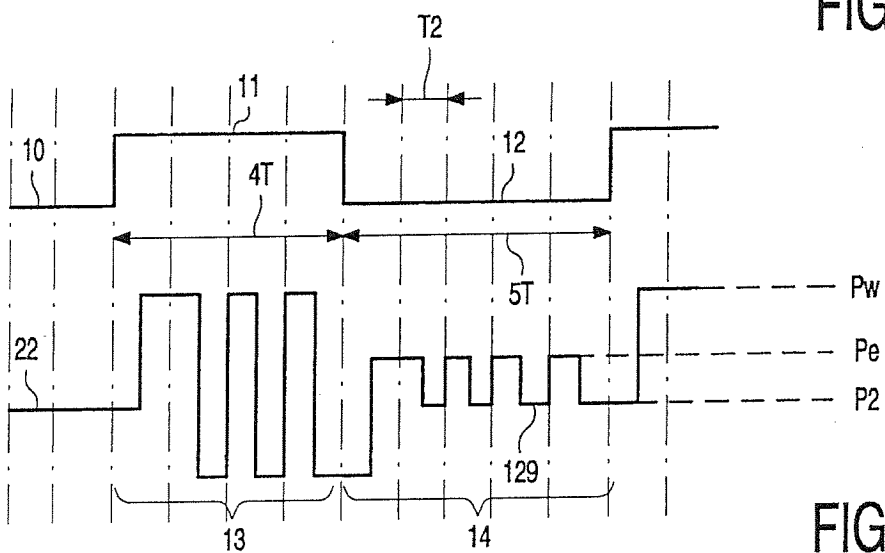


FIG. 1B

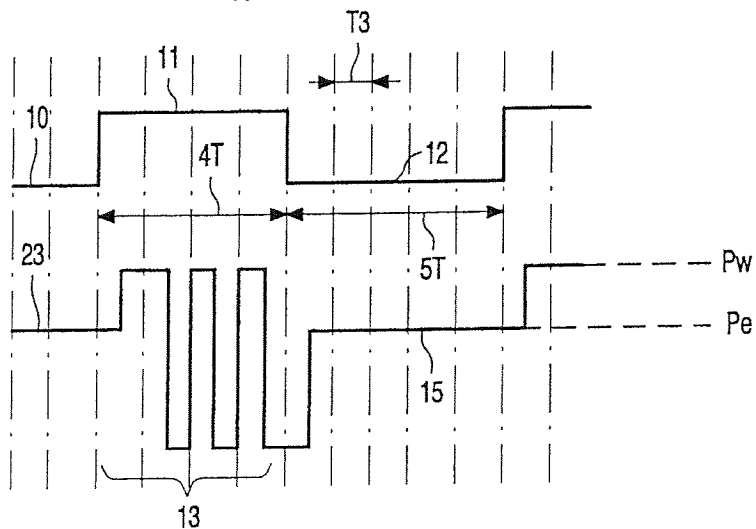


FIG. 1C

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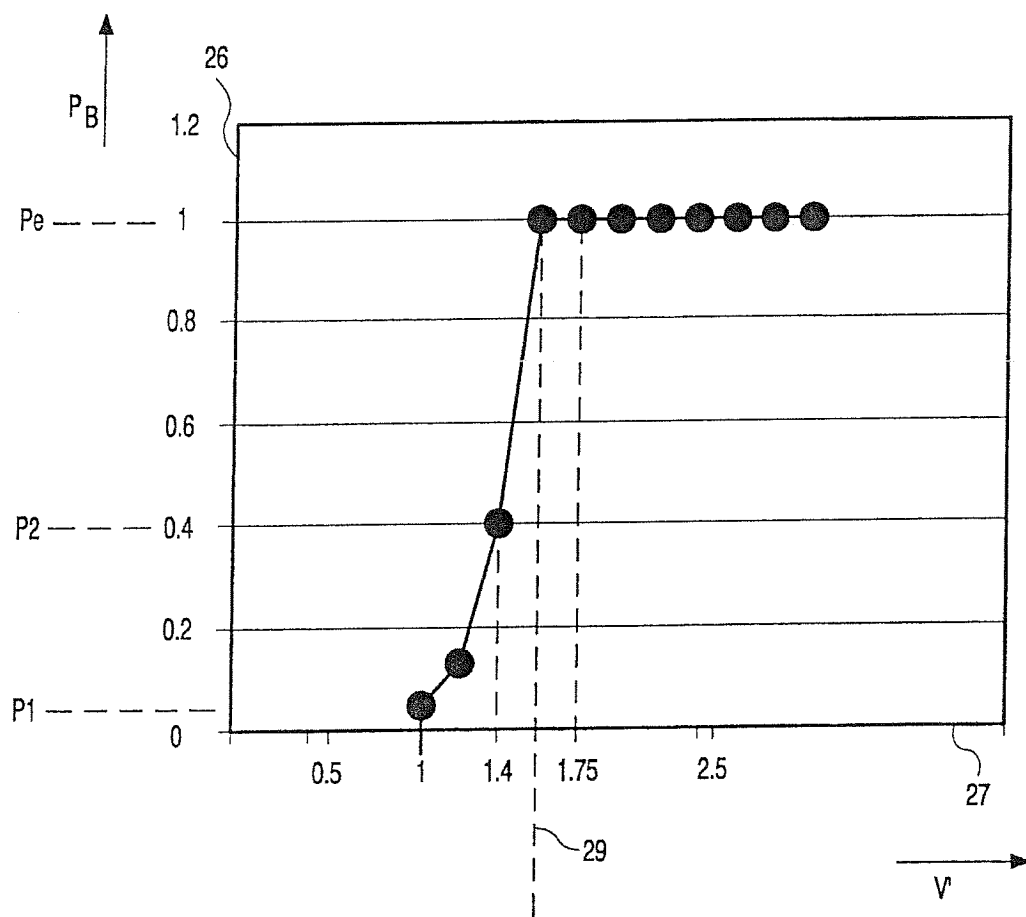


FIG. 2

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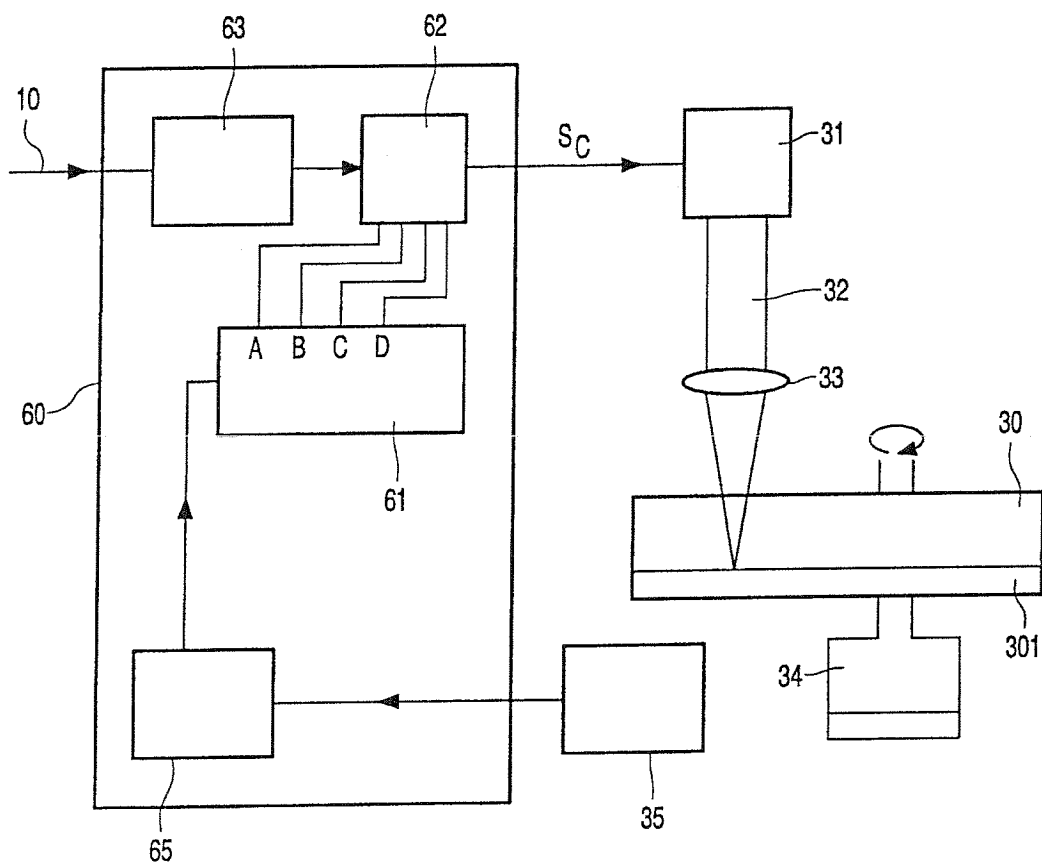


FIG. 3

## INTERNATIONAL SEARCH REPORT

International Application No.

PCT/EP 01/04457

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G11B7/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G11B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	PATENT ABSTRACTS OF JAPAN vol. 000, no. 000 (P-0000) & JP 001 067725 A (SEIKO EPSON CORP), 14 March 1989 (1989-03-14) abstract	1,7
A	US 5 291 470 A (NISHIUCHI KENICHI ET AL) 1 March 1994 (1994-03-01) cited in the application column 10, line 30 - line 59 column 11, line 55 -column 12, line 36; figures 12,18	1,7
A	EP 0 286 126 A (HITACHI LTD) 12 October 1988 (1988-10-12) column 15, line 15 -column 16, line 46; claim 9; figures 4A,4B	1,7
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Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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## INTERNATIONAL SEARCH REPORT

International Application No

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A	WO 98 36411 A (KONINKLIJKE PHILIPS ELECTRONICS N.V.) 20 August 1998 (1998-08-20) page 7, line 10 -page 8, line 9; figures 1A,1B ---	1,7
A	US 5 818 808 A (TAKADA KEN'ICHI ET AL) 6 October 1998 (1998-10-06) column 9, line 15 - line 17 column 9, line 51-63; figures 3-7 column 9 -----	1,7

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